

FIGURE 1

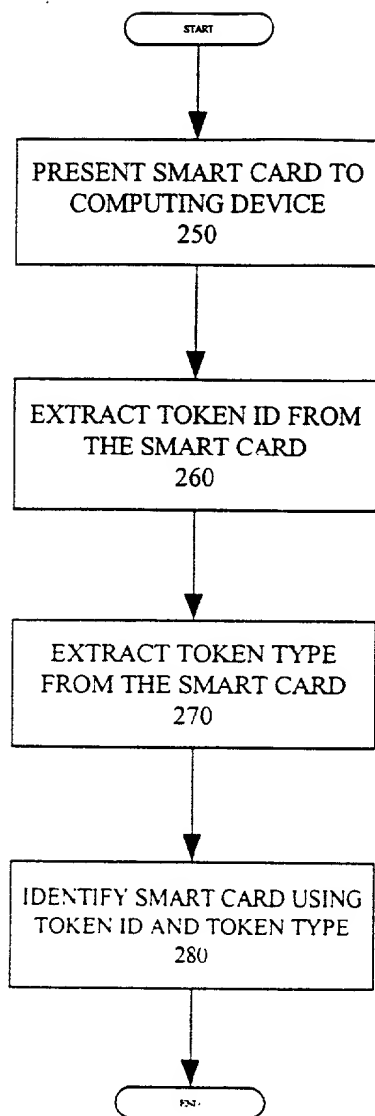


FIGURE 2A

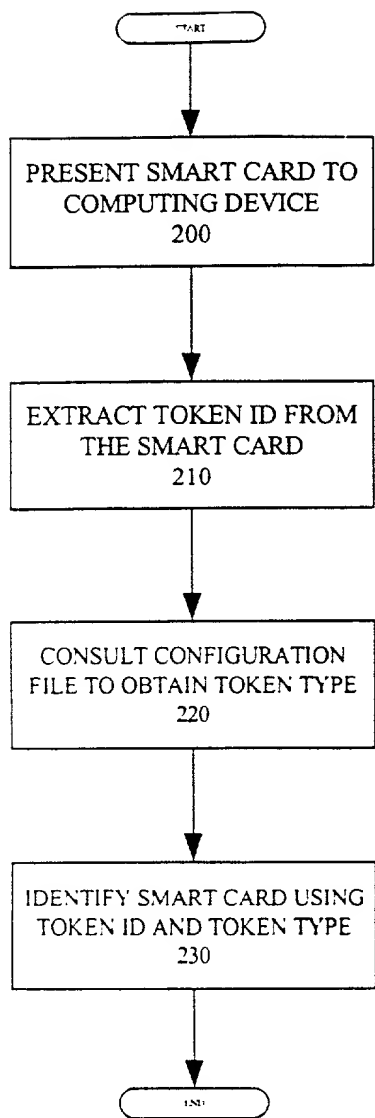


FIGURE 2B

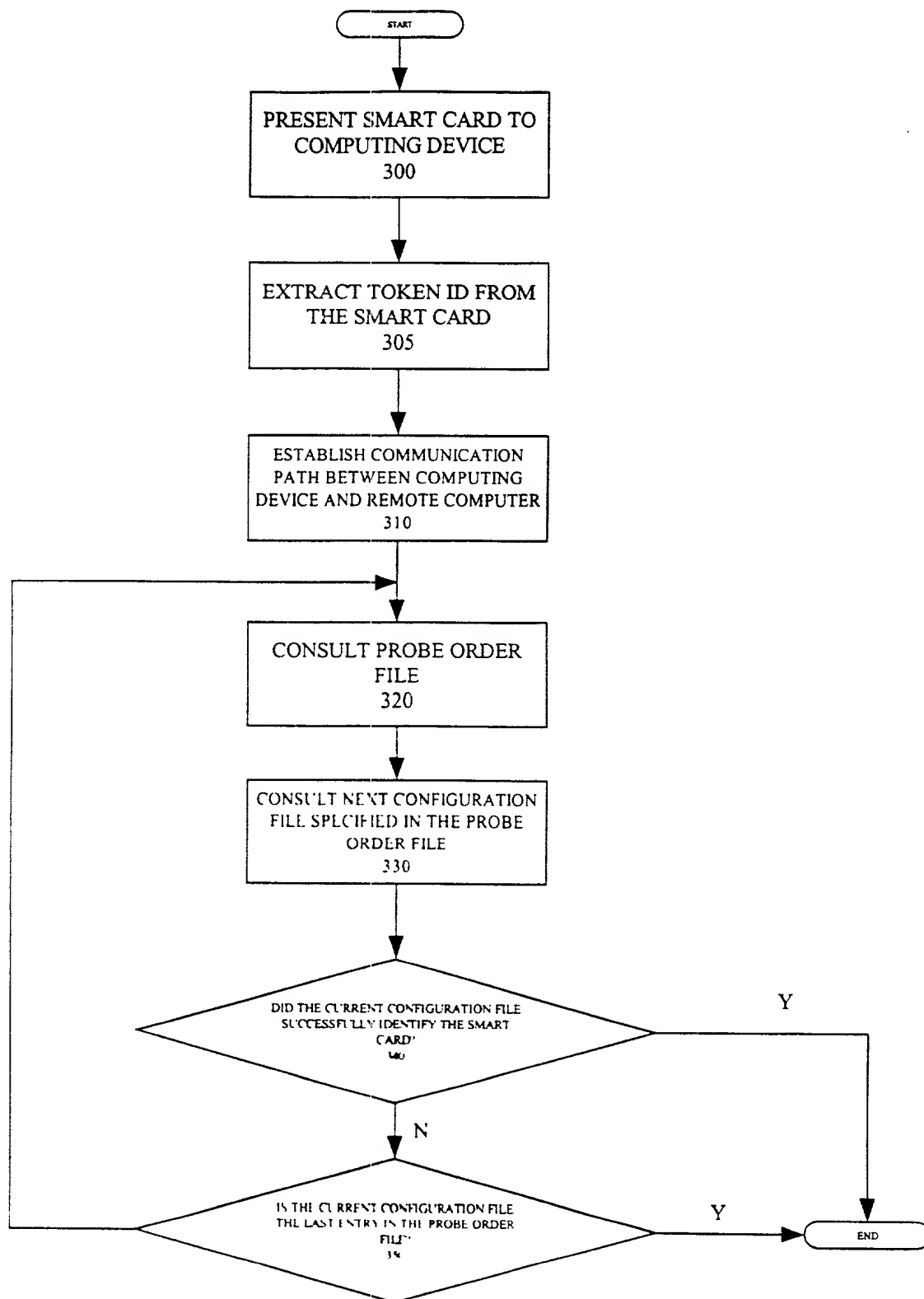


FIGURE 3

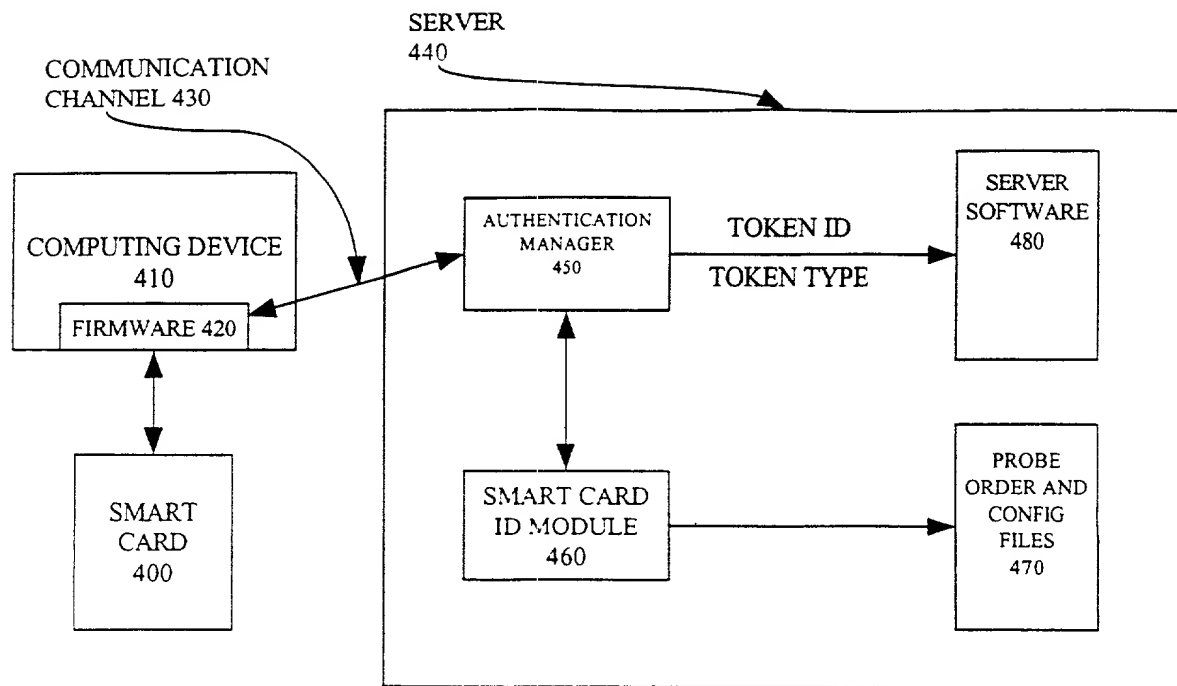
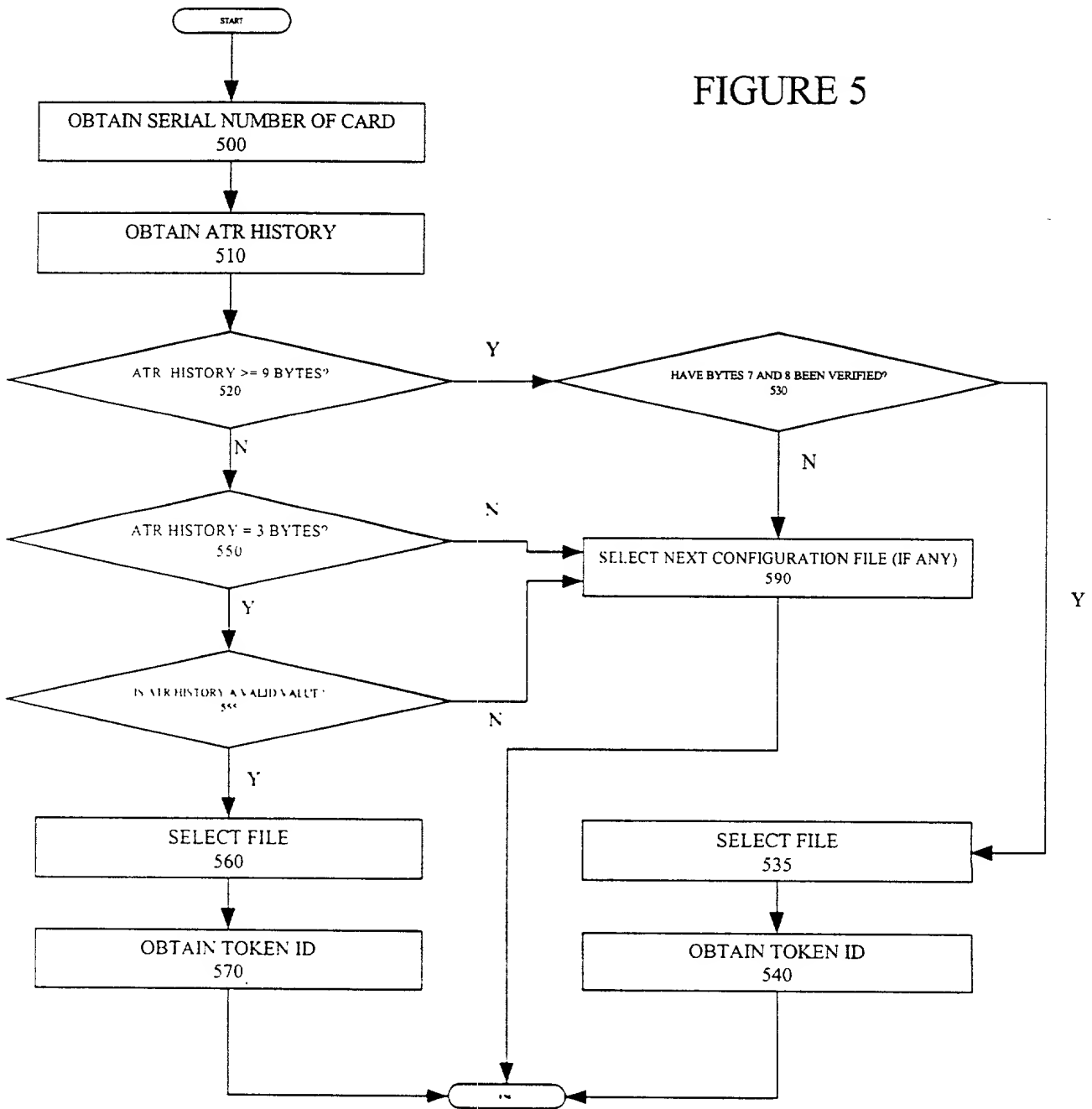


FIGURE 4

FIGURE 5



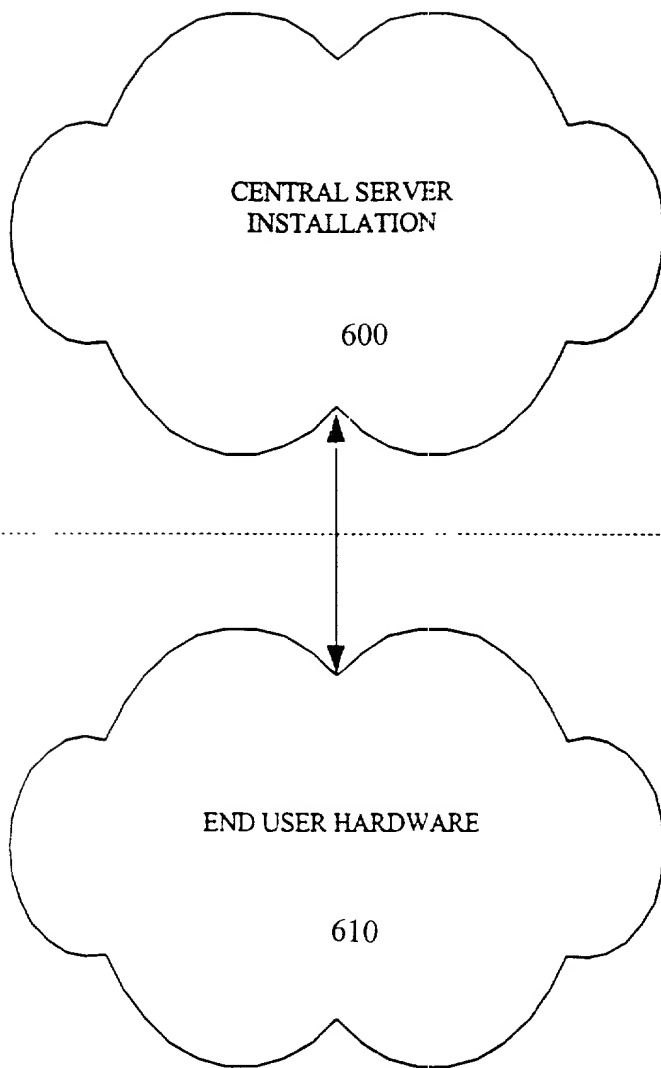


FIGURE 6

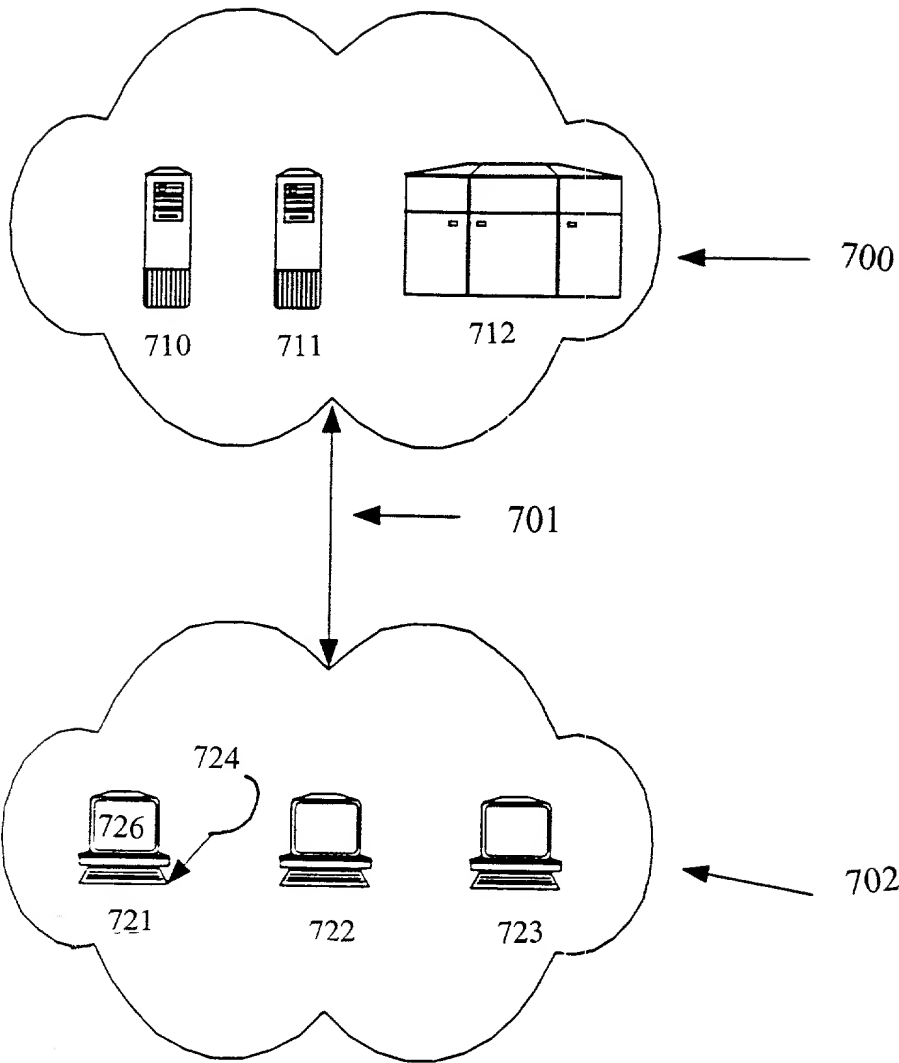


FIGURE 7



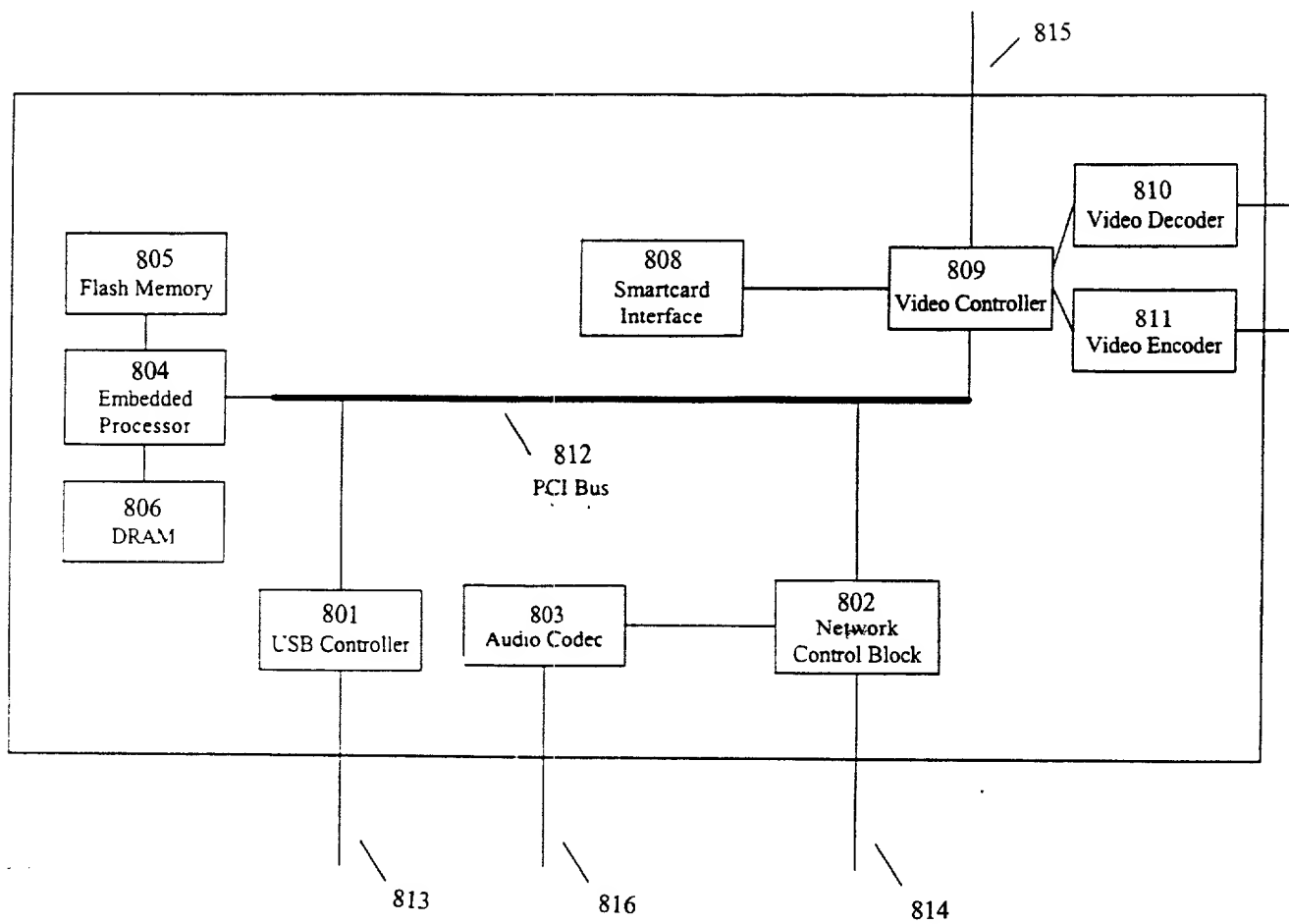


Figure 8: HID

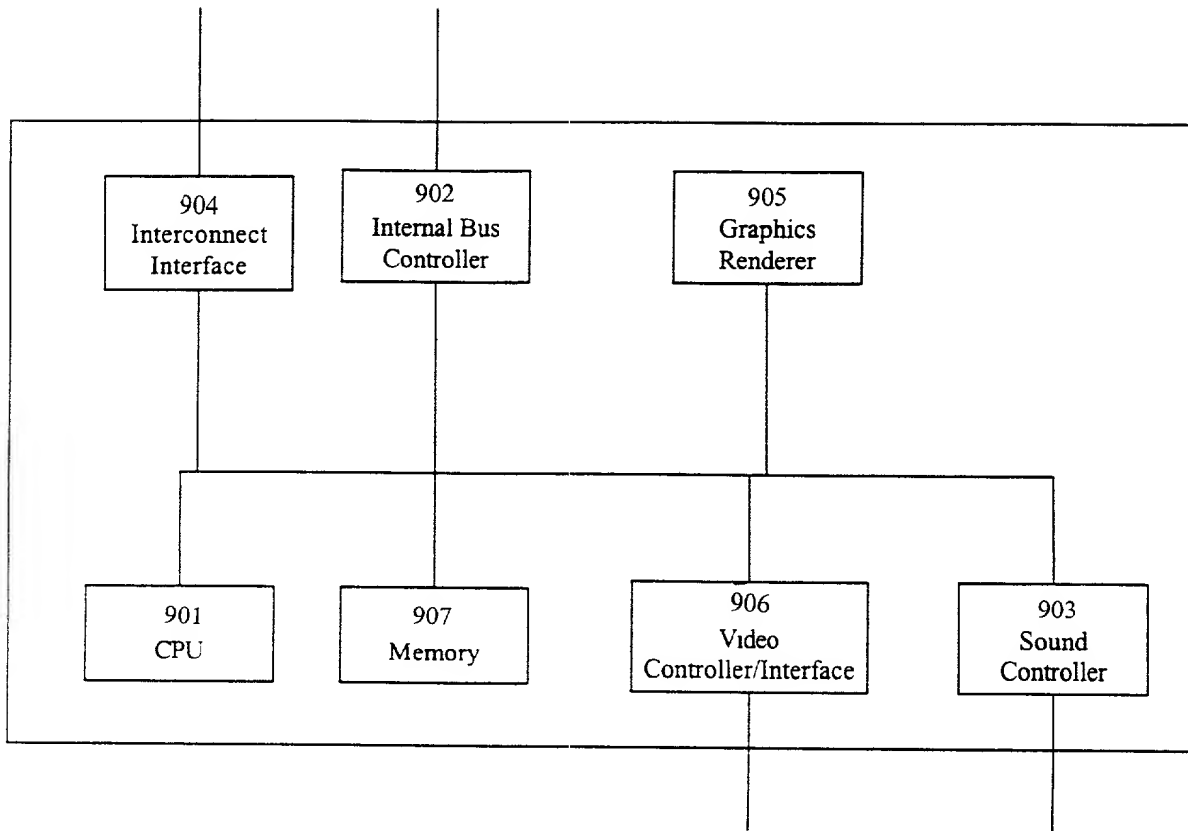


Figure 9 : HID Single Chip Implementation

FIG. 10 is a block diagram of a computer system 1000 in accordance with the present invention.

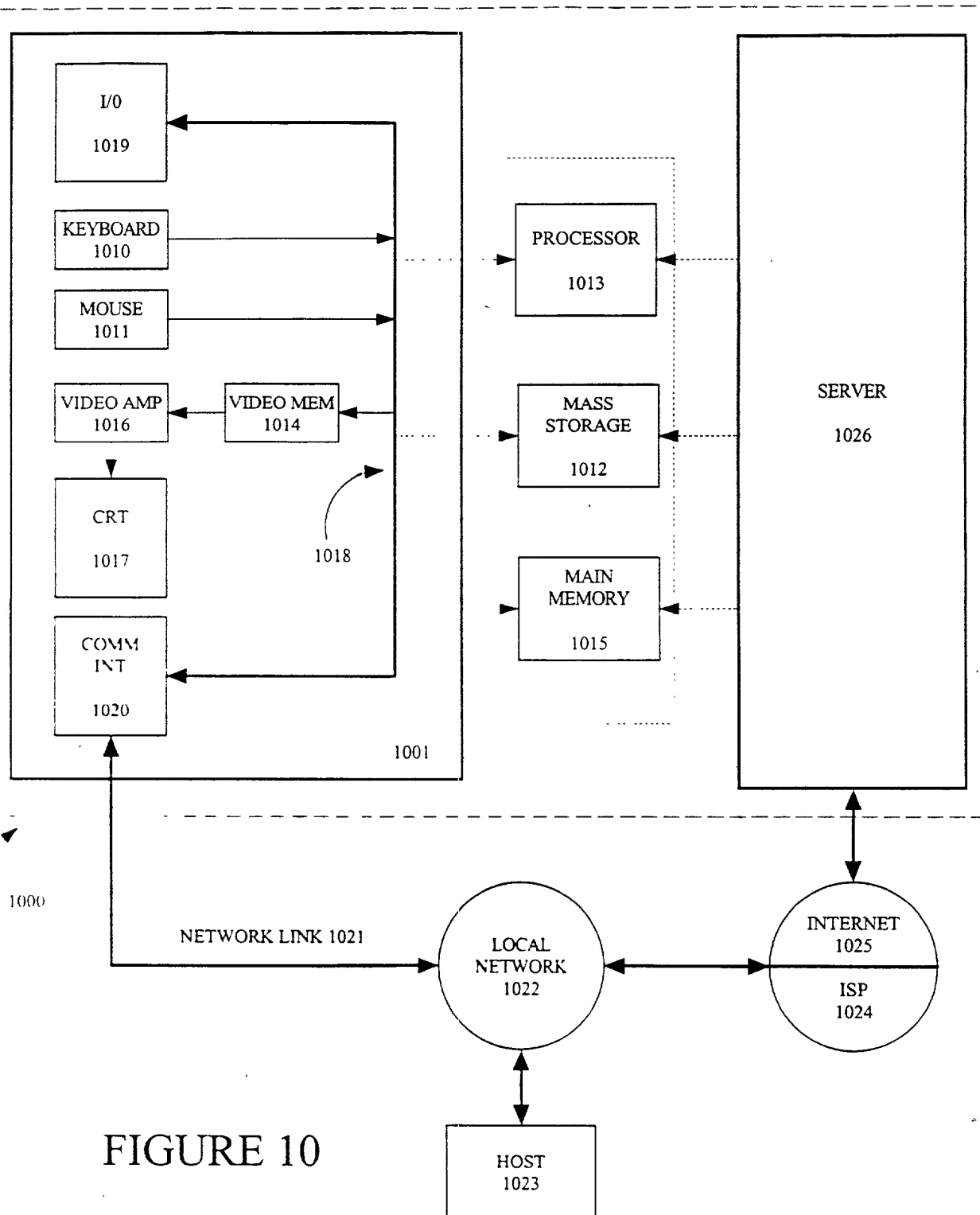


FIGURE 10